

Figure 1
(Prior Art)

Figure 3 is a timing diagram showing eight digital signals over time. The signals are labeled on the left: \emptyset_1 , \emptyset_2 , \emptyset_3 , \emptyset_4 , $\emptyset_1 + \emptyset_4$, $\emptyset_2 + \emptyset_3$, $\emptyset_1 + \emptyset_3$, and Voltage Asserted on 220. The signals are periodic square waves. \emptyset_1 has a period of 200 ns. \emptyset_2 , \emptyset_3 , and \emptyset_4 have a period of 100 ns. The sum signals ($\emptyset_1 + \emptyset_4$, $\emptyset_2 + \emptyset_3$, $\emptyset_1 + \emptyset_3$) have a period of 100 ns. The 'Voltage Asserted on 220' signal is a trapezoidal wave with a period of 100 ns. A scale bar at the bottom left indicates 0 ns and 200 ns.

Figure 3

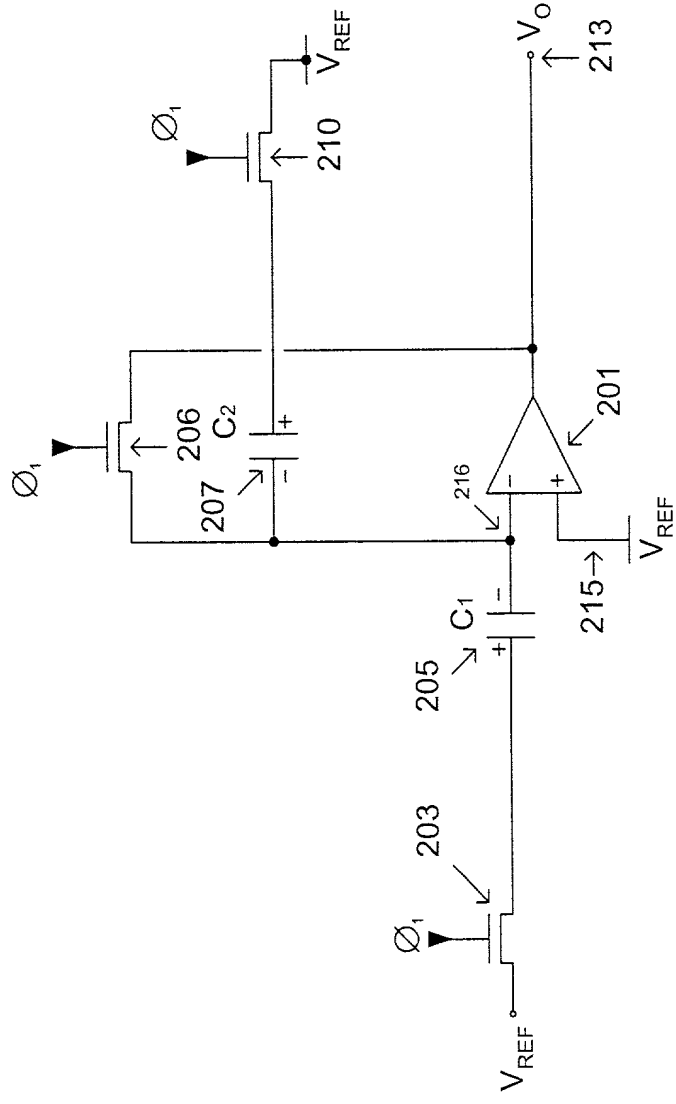


Figure 4

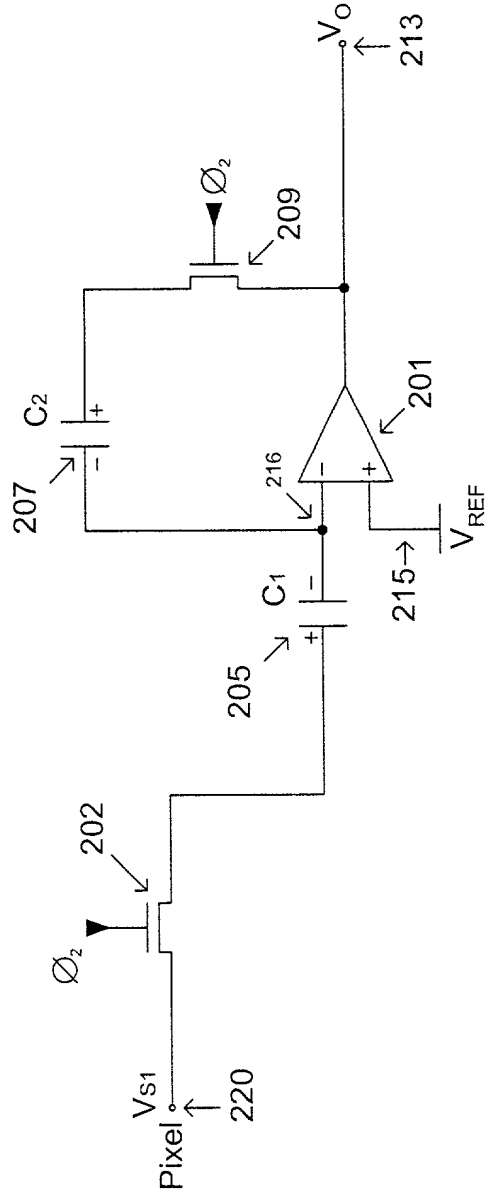


Figure 5

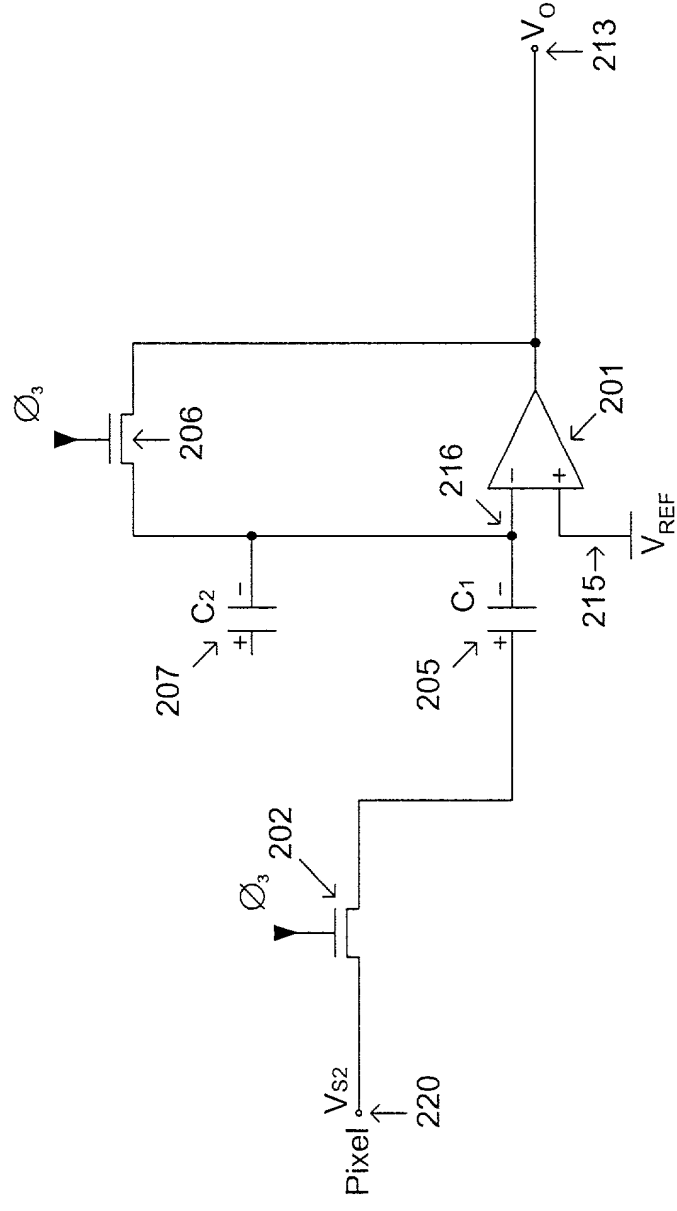


Figure 6

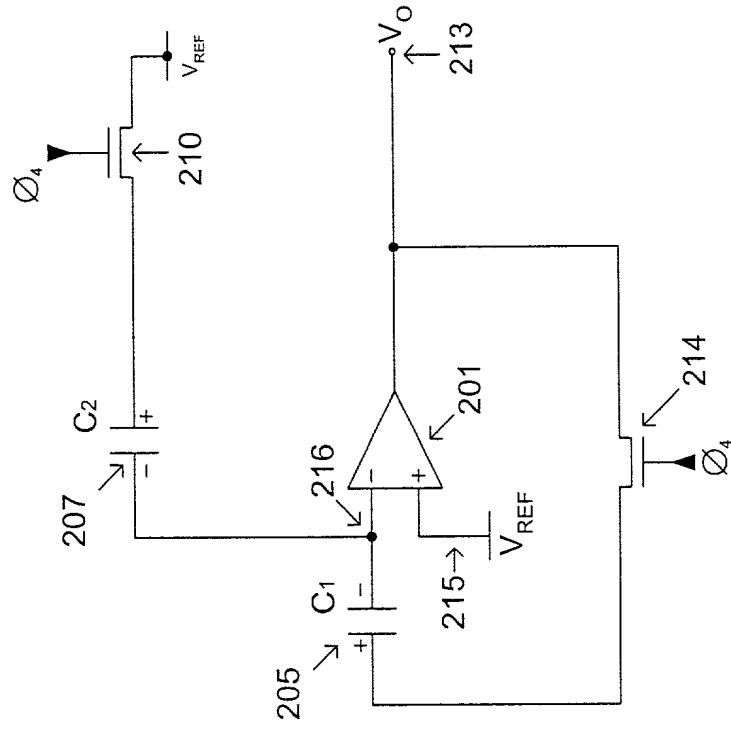


Figure 7